

Substitute for Form 1449 A & B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Sheet	1	of	1	Application Number	Div of 09/55,152 10/627,632
				Confirmation Number	Not yet assigned
				Filing Date	July 28, 2003
				First Named Inventor	Takanori SAEKI
				Art Unit	Not yet assigned 2816
				Examiner Name	Not yet assigned TON
				Attorney Docket Number	Q76734

U.S. PATENT DOCUMENTS

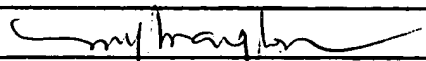
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
MT		US 6,184,736	327/245	02-2001	Wissell et al.
MT		US 6,137,336	327/295	10-2000	Baba et al.
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
MT		JP	8-237091	—	09-13-1996		
MT		JP	8-137091	—	05-13-1996		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
MT		J. Han et al., "Skew Minimization Techniques for 256M-bit Synchronous DRAM and Beyond", Symposium on VLSI Circuits Digest of Technical Papers (1998), pp. 192-193	
MT		R.B. Watson et al., "Clock Buffer Chip with Absolute Delay Regulation Over Process and Environmental Variations", Proc. of IEEE, Custom Integrated Circuits Conference (1992), pp. 25.2.1-25.2.5	
MT		Y. Okajima, et al., "Digital Delay Locked Loop and Design Technique for High Speed Synchronous Interface", IEICE Trans. Electron, Vol. E79-C, No. 6, June 1996, pp. 798-807	

Examiner Signature		Date Considered	04/28/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to indicate here if English language Translation is attached.